

326-40

326 40

Class

Subclass

ISSUE CLASSIFICATION

5744980

UTILITY
SERIAL
NUMBER 68504 U.S. PTO
08603597

PATENT DATE

100 73 1998

PATENT
NUMBERSERIAL NUMBER
08/603,597FILING DATE
02/16/96CLASS
327SUBCLASS
41GROUP ART UNIT
2504

EXAMINER

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VERIFIED

None RRR

FOREIGN/PCT APPLICATIONS***
VERIFIED

None RRR

FOREIGN FILING LICENSE GRANTED 07/29/96

Foreign priority claimed
35 USC 119 conditions met ☐ yes ☒ noAS
FILEDSTATE OR
COUNTRY
CASHEETS
DRWGS.
4TOTAL
CLAIMS
14INDEP.
CLAIMS
2FILING FEE
RECEIVED
\$880.00ATTORNEY'S
POCKET NO.
ACT-185Verified and Acknowledged ☒ yes ☐ no
Examiner's Initials RRR

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TITLE

FLEXIBLE, HIGH-PERFORMANCE STATIC RAM ARCHITECTURE FOR
FIELD-PROGRAMMABLE GATE ARRAYS

U.S. DEPT. OF COMM./PAT. & TM—PTO-436L (Rev.12-94)

PARTS OF APPLICATION
FILED SEPARATELY

NOTICE OF ALLOWANCE MAILED

Richard Roseen

Assistant Examiner

CLAIMS ALLOWED

Total Claims

Print Claim

14

1

ISSUE FEE

Amount Due

Date Paid

1290.00

8-18-97

Edward Weston

EDWARD WESTON
SUPERVISOR OF PATENT EXAMINERS

Primary Examiner

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

4

5

1

ISSUE
BATCH
NUMBER

P29

Label
Area

PREPARED FOR ISSUE

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(FACE)

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